



# INFORMATION DISCLOSURE CITATION IN AN APPLICATION

APR 30 2001

(in several sheets if necessary)

Divisional Application (Optional)

Continuation Application (Optional)

CS-99-24

Application Number

09/755,572

Applicant

Ting Cheong Ang et al.

Filing Date

01/08/01

Group Art Unit

## U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
SF	5504033	4/2/96	Bajor et al.	437	67	11/15/94
SF	6063652	5/16/00	Kim	438	155	2/25/99
SF	5591650	1/7/97	Hsu et al.	437	21	6/8/95
SF	5874328	2/23/99	Liu et al.	438	199	6/30/97
SF	5674760	10/7/97	Heng	437	24	2/26/96
SF	5930605	7/27/99	Mistry et al.	438	149	7/16/97

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Portmanteau Pages, Etc.)

SF	S. Wolf, "Silicon Processing for the VLSI Era", Vol. 2, Lattice Press, Sunset Beach, CA, c. 1990, pp. 66-67.

EXAMINER

DATE CONSIDERED

5/31/02

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through